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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Hiroshi Kondo et al

Serial No.:

09/723,768

Conf. No.:

6836

Filed:

November 28, 2000

For:

APPARATUS AND METHOD FOR PREVENTING AN ERRONEOUS OPERATION AT THE TIME OF

DETECTION OF A SYSTEM FAILURE

Art Unit:

2113

Examiner:

Puente, Emerson C.

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.

August 21, 2006

Date

Registration No. 41,895 Attorney for Applicant(s)

ISSUE FEE AND PUBLICATION FEE TRANSMITTAL

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Respectfully submitted,

GREER, BURNS & CRAIN, LTD.

By

B. Joe Kim

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August 21, 2006

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